

PATENT  
Atty. Dkt. No.: 1458-P04919

**IN THE SPECIFICATION**

Please amend the related applications section at page 1 as follows:

**CROSS-REFERENCE TO RELATED APPLICATIONS**

The present invention is related to those disclosed in the following United States Patent Applications:

Serial No. 09/796,328, filed on February 28, 2001, entitled "INTEGRATED CIRCUIT HAVING PROGRAMMABLE VOTAGE LEVEL LINE DRIVERS AND METHOD OF OPERATION";

Serial No. 09/796,660, filed on February 28, 2001, entitled "REDUCED NOISE LINE DRIVERS AND METHOD OF OPERATION";

Serial No. ~~[NATH15-04920]~~ 09/845,455, filed concurrently herewith, entitled "BUS ARBITRATOR SUPPORTING MULTIPLE ISOCHRONOUS STREAM IN A SPLIT TRANSACTIONAL UNIDIRECTIONAL BUS ARCHITECTURE AND METHOD OF OPERATION" (now U.S. Patent No. 6,813,673).

The above applications are commonly assigned to the assignee of the present invention. The disclosures of these related patent applications are hereby incorporated by reference for all purposes as if fully set forth herein.